

**E 6454**



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Reg. No.....

Name.....

**B.C.A. DEGREE (C.B.C.S.S.) EXAMINATION, MAY 2024**

**Fourth Semester**

**MICROPROCESSOR AND PC HARDWARE**

(2013–2016 Admissions)

Time : Three Hours

Maximum Marks : 80

**Part A**

*Answer all questions.  
Each question carries 1 mark.*

1. How many bits are there in the data bus of the Intel 8085 microprocessor ?
2. What is DMA in 8085 ?
3. What is MVI instruction in 8085 ?
4. How many bits are there in the data bus of 8086 ?
5. How many general data registers are there in 8086 ?
6. What is the PUSH operation in 8086 ?
7. What is VFAT ?
8. What is physical memory ?
9. What is upper memory area ?
10. What is extended memory ?

(10 × 1 = 10)

**Part B**

*Answer any eight questions.  
Each question carries 2 marks.*

11. What is the role of the accumulator in 8085 ?
12. How many instructions are there in 8085 ?
13. What is RET instruction in 8085 ?
14. What are the four segment registers in 8086 ?
15. What is the POP operation in 8086 ?
16. What are the basic components of a motherboard ?

**Turn over**





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17. What is a chip set ?
18. What is disk formatting ?
19. What is AGP ?
20. What is a memory bus ?
21. What is high memory area ?
22. What is expanded memory ?

(8 × 2 = 16)

### Part C

*Answer any **six** questions.  
Each question carries 4 marks.*

23. Briefly, explain the addressing modes of 8085.
24. Briefly, explain the addressing modes of 8086 ?
25. Briefly, explain various pointers in 8086.
26. Briefly, explain various conditional and control flags in 8086.
27. Briefly, explain the motherboard selection criteria.
28. Briefly, explain super I/O chip.
29. Briefly, explain ROM BIOS.
30. Briefly, explain NTFS disk.
31. Briefly, explain high memory area.

(6 × 4 = 24)

### Part D

*Answer any **two** questions.  
Each question carries 15 marks.*

32. Explain the architecture of 8085 with illustrations.
33. Explain the architecture of 8086 with illustrations.
34. Explain the construction and working of different types of system buses.
35. Explain different types of memory modules with illustrations.

(2 × 15 = 30)

